FPGA firmware from OberonSystem.com targeting various FPGAs. (ISE 14.6.)
Compiled by WS on Oct/18/2014.

#		Family	BRAM kB 2)	Capacity Slices	Occupied Slices	Used %	Speed MHz 3)	Wpack	Board 5)
1	XC3S200 1)	Spartan-3	24	1,920	1,832	95.4%	37.3	Y	S3 Starter
2	XC3S1000	Spartan-3	48	7,680	2,119	27.6%	37.3	Y	
3	XC6SLX4	Spartan-6	24	600	no fit	no fit	n/a	**Y**	
4	XC6SLX9	Spartan-6	64	1,430	836	58.5%	55.7	**Y**	Papilio, Pipistrello
5	XC6SLX25	Spartan-6	104	3,758	831	22.1%	55.7	**Y**	Pipistrello
6	XC6SLX45	Spartan-6	232	6,822	866	12.7%	55.7	**Y**	Pipistrello
7	XC6SLX75	Spartan-6	344	11,662	861	7.4%	55.7	**Y**	
8	XC6SLX100	Spartan-6	536	15,822	884	5.6%	55.7	N	
9	XC6SLX150	Spartan-6	536	23,038	840	3.6%	55.7	N	
10	XC7A35T	Artix-7	200	5,200	*1,096*	*21.1%*	*92.2*	*4)*	
11	XC7A50T	Artix-7	300	8,150	*1,096*	*13.4%*	*92.2*	*4)*	
12	XC7A75T	Artix-7	420	11,800	*1,096*	*9.3%*	*92.2*	*4)*	
13	XC7A100T	Artix-7	540	15,850	1,096	6.9%	92.2	**Y**	Nexys-4
14	XC7A200T	Artix-7	1,460	33,650	1,024	3.0%	92.2	**Y**	

1) Spartan-3 XC3S200 was used to develop and run the firmware.

2) Usable BRAM = data sheet BRAM in kbits / 9, because parity bits are not useful.

3) Nominal max speed in the highest speed grade from the synthesis report.

4) Though expected to be WebPack parts, Artix-7 35T, 50T, and 75T are not on a part list in ISE 14.6. Their performance was estimated.

5) There are many more boards than listed here. The listed ones are just examples..